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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

None TT

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

None TT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CO	SHEETS DRAWING 6	TOTAL CLAIMS <del>30</del> 27	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Tracy</i> Examiner's Signature	<i>TT</i> Initials			

## ADDRESS

22879

## TITLE

System and method for controlling analysis of multiple instantiations of circuits in hierarchical VLSI circuit designs

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